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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/675,166	09/30/2003	John K. Walton	EMC2-122PUS	5917
45456	7590	08/13/2007	EXAMINER	
RICHARD M. SHARKANSKY PO BOX 557 MASHPEE, MA 02649			SORRELL, ERON J	
ART UNIT		PAPER NUMBER		
2182				
MAIL DATE		DELIVERY MODE		
08/13/2007		PAPER		

Please find below and/or attached an Office communication concerning this application or proceeding.

The time period for reply, if any, is set in the attached communication.

Office Action Summary	Application No.	Applicant(s)
	10/675,166	WALTON ET AL.
Examiner	Art Unit	
Eron J. Sorrell	2182	

-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address --

A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 3 MONTH(S) OR THIRTY (30) DAYS, WHICHEVER IS LONGER, FROM THE MAILING DATE OF THIS COMMUNICATION.

- Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication.
 - If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication.
 - Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133). Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).

Status

- 1) Responsive to communication(s) filed on 07 May 2007.

2a) This action is **FINAL**. 2b) This action is non-final.

3) Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under *Ex parte Quayle*, 1935 C.D. 11, 453 O.G. 213.

Disposition of Claims

- 4) Claim(s) 5 and 9 is/are pending in the application.
4a) Of the above claim(s) _____ is/are withdrawn from consideration.

5) Claim(s) _____ is/are allowed.

6) Claim(s) 5 and 9 is/are rejected.

7) Claim(s) _____ is/are objected to.

8) Claim(s) _____ are subject to restriction and/or election requirement.

Application Papers

- 9) The specification is objected to by the Examiner.

10) The drawing(s) filed on 30 September 2003 is/are: a) accepted or b) objected to by the Examiner.

 Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).

 Replacement drawing sheet(s) including the correction is required if the drawing(s) is objected to. See 37 CFR 1.121(d).

11) The oath or declaration is objected to by the Examiner. Note the attached Office Action or form PTO-152.

Priority under 35 U.S.C. § 119

- 12) Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
a) All b) Some * c) None of:
1. Certified copies of the priority documents have been received.
2. Certified copies of the priority documents have been received in Application No. _____.
3. Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)).

* See the attached detailed Office action for a list of the certified copies not received.

Attachment(s)

- 1) Notice of References Cited (PTO-892) 4) Interview Summary (PTO-413)
2) Notice of Draftsperson's Patent Drawing Review (PTO-948) Paper No(s)/Mail Date. _____
3) Information Disclosure Statement(s) (PTO/SB/08)
Paper No(s)/Mail Date _____ 5) Notice of Informal Patent Application
6) Other: _____

DETAILED ACTION

Examiner's Remarks

1. Claims 5 and 9 are currently pending.
2. The applicant's amendment to claims 5 and 9 are sufficient to overcome the 112-2nd paragraph rejection set forth in the previous office action.

Response to Arguments

3. It is noted that the applicant refers to claim 6 in the applicant's arguments, however claim 6 has been canceled. From the substance of the argument, it is the opinion of the Examiner that the applicant is arguing the merits of claim 9.
4. Applicant's arguments with respect to claims 5 and 9 have been considered but are moot in view of the new ground(s) of rejection.

Information Disclosure Statement

5. The listing of references in the specification is not a proper information disclosure statement. 37 CFR 1.98(b) requires a list of all patents, publications, or other information submitted for consideration by the Office, and MPEP § 609.04(a) states, "the list may not be incorporated into the

specification but must be submitted in a separate paper."

Therefore, unless the references have been cited by the examiner on form PTO-892, they have not been considered.

Claim Rejections - 35 USC § 103

6. The following is a quotation of 35 U.S.C. 103(a) which forms the basis for all obviousness rejections set forth in this Office action:

(a) A patent may not be obtained though the invention is not identically disclosed or described as set forth in section 102 of this title, if the differences between the subject matter sought to be patented and the prior art are such that the subject matter as a whole would have been obvious at the time the invention was made to a person having ordinary skill in the art to which said subject matter pertains. Patentability shall not be negatived by the manner in which the invention was made.

7. Claim 5 is rejected under 35 U.S.C. 103(a) as being unpatentable over Calvignac et al. (U.S. Patent No. 6,044,079 hereinafter "Calvignac") in view of Pierson (U.S. Pub. No. 2003/0048781) and further in view of Zhao et al. (U.S. Pub. No. 2005/0147034 hereinafter "Zhao").

8. Referring to claim 5, Calvignac teaches a queuing system comprising:

 a plurality of interconnected directors (items 86, figure 5), each one of the directors having:

an input/output interface section (item 94, figure 5) receiving information from a source thereof and returning information to interface to such source (see lines 50-65 of column 8); and,

a microprocessor processing information sent thereto from a remote one of the directors, each one of the microprocessors having a CPU (item 90, figure 5) and a CPU memory (item 92, figure 5) such CPU memory storing a queue for inbound information passed to such director for processing therein such information being sent to the remote director from an originating one of the directors (see lines 50-65 of column 8, note the Examiner is construing the combination of elements 90 and 92 to be the claimed microprocessor; the memory 92 stores incoming data to be processed by the processor and outgoing data processed by the processor that is ready for transmission).

Calvignac fails to teach each one of the input/output sections includes a queue for outbound information being returned to the source through such originating one of the directors after being processed by the microprocessor of such remote one of the directors.

Pierson teaches, in an analogous system, the input/output sections has a queue for outbound information (see paragraph 45).

It would have been obvious to one of ordinary skill in the art at the time of the applicant's invention to modify the system of Calvignac with the above Pierson. One of ordinary skill in the art would have been motivated to make such modification in order to allow data to accumulate before sending the data to its destination improving the data transfer processing as suggested by Pierson (see paragraph 45).

The combination of Calvignac and Pierson fails to teach that each one of the directors includes a translation table, such table storing at a location thereof corresponding to each one of the remote directors a producer index for the queue of such remote director and a consumer index for such one of the remote directors and an index into the table is a function of the receiving information in a queue entry in the queue and derives the location of the source from the table.

Zhao teaches, in an analogous switching system, the directors including a translation table storing a producer index and a consumer index for the remote director (see figure 3, note the head and tail pointers correspond to applicant's producer and consumer index) and an index into the table is a function of the receiving information and derives a location of the source from the table (see paragraph 37, note the FLID comprises the source address).

It would have been obvious to one of ordinary skill in the art at the time of the applicant's invention to modify the combination of Calvignac and Zhao with the above teachings of Zhao. One of ordinary skill in the art would have been motivated to make such modification in order to improve the scheduling of the data through the system as suggested by Zhao (see paragraph 35).

9. Claim 9 is rejected under 35 U.S.C. 103(a) as being unpatentable over Calvignac in view of Pierson.

10. Referring to claim 9, Calvignac teaches a queuing system comprising:

 a plurality of interconnected directors (items 86, figure 5), each one of the directors having:

 an input/output interface section (item 94, figure 5) receiving information from a source thereof and returning information to interface to such source (see lines 50-65 of column 8); and,

 a microprocessor processing information sent thereto from a remote one of the directors, each one of the microprocessors having a CPU (item 90, figure 5) and a CPU memory (item 92, figure 5) such CPU memory storing a queue for inbound

information passed to such director for processing therein such information being sent to the remote director from an originating one of the directors (see lines 50-65 of column 8, note the Examiner is construing the combination of elements 90 and 92 to be the claimed microprocessor; the memory 92 stores incoming data to be processed by the processor and outgoing data processed by the processor that is ready for transmission).

Calvignac fails to teach each one of the input/output sections includes a queue for outbound information being returned to the source through such originating one of the directors after being processed by the microprocessor of such remote one of the directors.

Pierson teaches, in an analogous system, the input/output section has a queue for outbound information (see paragraph 45).

It would have been obvious to one of ordinary skill in the art at the time of the applicant's invention to modify the system of Calvignac with the above Pierson. One of ordinary skill in the art would have been motivated to make such modification in order to allow data to accumulate before sending the data to its destination improving the data transfer processing as suggested by Pierson (see paragraph 45).

Conclusion

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Eron J. Sorrell whose telephone number is 571 272-4160. The examiner can normally be reached on Monday-Friday 8:00AM - 4:30PM.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Kim Huynh can be reached on 571-272-4147. The fax phone number for the organization where this application or proceeding is assigned is 571-273-8300.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see <http://pair-direct.uspto.gov>. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free). If you would like assistance from a USPTO Customer Service Representative or access to the automated information system, call 800-786-9199 (IN USA OR CANADA) or 571-272-1000.

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